1 Introduction

The egg incubator is an equipment designed to give eggs the necessary condition for hatching such as temperature, humidity, ventilation and sanitation for a specified period of time before hatching. This paper aimed at improving the productivity of the Nation and the provision of a cheap source of protein. It is also aimed at simplifying the work in the farm and at the same time increasing the output. The incubator is constructed in form of a box that is made up of plywood for retaining the heat generated by the bulbs inside the box (chamber) and has two holes on both sides for proper ventilation. There is a water tray inside the chamber (box) which is in the incubator for the control of humidity, and the incubator has the capacity of hatching 50 eggs, which is placed inside the chamber by means of net arrangement. The supply to the control circuit of this incubator is 240 V (AC) which will step down to 24 V (AC) and later being rectified by bridge rectifiers to 22 or 23 DC voltage that will pass through the circuit. The bulbs are installed in the chamber, to provide heat at a conditioned temperature ranging between 32 to 39°C based on other ranges used [3]. The power-supply unit from the first stage which in employed to power the electronic circuit of the system. This stage provides the necessary DC voltage current required by the passive and component of the control unit. This sensing and comparators stage help to compare two voltages at its input and produce on output, which is a difference of the two input voltage. The next stage is switching stage where the switching actions in performed on the relay by opening and closing contact. Temperature control as far as this design is concerned, depends on the actions of the transducers mentioned. Those transducers are, for example, the thermistor which is the back bone actions of the transducers mentioned. Those transducers are, for example, the thermistor which is the back bone of this design as compared with other designs which were done by Aliyu and Idris[1]. However, temperature control could either be on open loop control system or close loop control system, which can be called the on-off control system[2].

The sensitivity of the thermistor and its reliability in an incubation system[2], it compelled the choice and also behaves as a close-loop control system, by either oning the circuit or offing the circuit depending on the temperature set (voltage equivalent) on the operational amplifier. In this design where a certain temperature is to be maintained and controlled, therefore, it does have a specific or reference temperature, as said earlier, which depends on the temperature set (voltage equivalent)[1].

The Fig. 1 shows a simplified block diagram of a temperature controlled system. The essential of any control system is that the output is dependent on the control operation, i.e. the system is an error actuated control sys-
In a close-loop controlled system, the output fed back and compared with the designed input, and necessary correction action is taken \[4\]. Thermistor (temperature sensor) is a temperature-sensitive resistor; it is a special thermal resistor with a negative temperature coefficient of resistance \[5\]. There are various types of temperature sensing devices like bimetallic strip, but the one in this project is thermistor with a negative temperature coefficient of resistance because it has so many advantages over other types of temperature’s sensors such as

1. Extremely high temperature sensitively,
2. Accuracy.

The circuit diagram of an egg incubator with automatic temperature control system is shown in Fig. 2. The power supply which is protected by the protective device (fuse) which fed from 240 V AC supply and step down to 24 V by the transformer. The secondary side (output) of the transformer is center tapped with output voltage of 24 V. Thus, 24 V is then rectified by using a bridge rectifier which consists of integrated four diodes, normally connected in bridge rectifier. Hence, the smoothing capacitor \(C_1\) and \(C_2\) in the circuit are used to filler the AC ripples flow into the circuit, so that almost perfect direct current (DC) is obtained at the output. The supply is of three rails power supply, that is producing \(\pm 12\) V and 0 V (ground) which are ideal for effective operation of OP-Amp used as a comparator in the circuit. Furthermore, Limiting resistors \(R_1\) and \(R_2\) are connected in Series to the LED and Zener diode. LED as a transducer converts voltage to light shows the power-supply unit is properly rectified while zener diode \((Z_1)\) is used in the circuit to stabilize, regulate and provide constant biasing voltage of 12 V to forward bias the transistor (Q1). Since the transistor (Q1) is permanently ON, which make the transistor (Q2) to be at saturated position and zero voltage at the base of Q2, this result that transistor Q2 not conducting, which, consequently, make the voltage at collector of Q2 to be almost 6 V (that is voltage formation produced by a potential divider formed by supply voltage and voltage at collector Q2). Limiting resistor \(R_3\) formed the divider point and voltage at this point refers to reference voltage (-) at comparator. Henceforth, voltage at pin 3 of the comparator is to be compared with reference voltage at pin 2 of Op-Amp. The compared voltage at pin 3 is determined by the potential divider formed by the thermistor (THM) and variable resistor (VR) in the circuit. Also non-reference voltage (+) will be determined by the amount of charging and discharging levels of capacitor \(C_3\) at a period of time.

According to the above explanation, non-reference voltage at pin 3 of Op-Amp will now produce voltage variance depending on changes in the thermistor resistance which is subjected to change by the applied heat in the chamber. The difference of the compared voltage \((\pm)\) produced an output voltage that formed square wave when tested through oscilloscope in a digital form represents 0 or 1. It is this output that will determine the operation of the next stage. The last action is performed by triggering the circuit either ON or OFF by PNP transistor (Q3) and consequently, energizes or de-energizes the relay that operates the bulb (Heater) inside the chamber. This is a simple way that the circuit works to attain the desired temperature in the chamber as it was presented by French \[3\].

\[3\]

**2 Methodology**

The flow chart of step by step design methodology is shown in Fig. 3. The methodology involved is the realization of the following stages; such as circuit design which consists of power-supply and rectification stage, stabilizing and amplifying stage, sensing and comparison stage, switching and heating stage.

![Flow chart of step by step design methodology](image)

**3 Circuit Design Analysis**

The analysis of this design is sub-divided into five major stages. These stages work hand-in-hand to produce desired function. The block diagram simply illustrates the conjunctural operation of the design. Thus, the main five stages are

1. Power-supply stage,
2. Stabilizing and amplifying stage,
3. Sensing and comparing stage,
4. Switching and,
5. Heating stage.

**3.1 Design Specifications**

The design was based on the following technical specifications. The input DC voltage is 12 V; the heat energy is 191 J; AC output is 220 V; the output power is 120 W; the frequency is 50 Hz; and the efficiency is 80%.
3.2 Power Supply Stage

The power-supply unit is made up of a transformer with full wave bridge rectifier and efficient filtering device for smoothing the ripples. The rectification is the first part to be considered by simple conversion of AC to DC, while filtering part allow the pure signal to flow for the optimum operation of an Op-Amp in the circuit. The generated DC voltage from 220 V AC supply that step down to 24 V with 500 mA current rating before flow to the rectification part[6].

3.3 Rectification

The objective of this part is to ensure AC signal is properly converted to digital, which is the base supply for electronics devices[7]. A full wave bridge rectifier used for the rectification and 14 V DC supply is produced. The following parameters illustrate the design analysis of it.

The percentage of voltage regulation (\(V_{\text{Reg}}\%\)) can be calculated via (1).

\[
V_{\text{Reg}}\% = \frac{V_n - V_f}{V_f} \times 100 \tag{1}
\]

where, \(V_n\) is the voltage at nominal load and \(V_f\) is the voltage at full load. Therefore, for a full wave rectifier used in this design, direct current supply (\(V_{dc}\)) is given as (2).

\[
V_{dc} = \frac{2V_m}{\pi} \tag{2}
\]

where, \(V_m\) is the peak of input AC voltage.

Another part in the power-supply stage is filtering, which is achieved by using electrolytic capacitors of 2200 \(\mu\)F and 25 V. This capacitor is purposely meant of filter the ripples flow into the circuit. For full wave rectification acquired above, and the analysis of it is as follows.

Filtering capacity (\(V_{r,\text{rms}}\)) can be calculated by using (3).

\[
V_{r,\text{rms}} = \frac{X_C V_{\text{rms}}}{R} \tag{3}
\]

or

\[
V_{r,\text{rms}} = \frac{V_{\text{rms}}}{2\pi f CR} \tag{4}
\]

where \(\pi\) is a constant value of 3.142, \(f\) is the frequency (50 Hz), \(CR\) is time constant for changing \(r\), \(V_{\text{rms}}\) is the rms voltage of the input AC supply.

3.4 Stabilizing Stage

The supply voltage (\(V_{dc}\)) passes through filtering capacitor, but as far as this part is concerned, the capacitors act as an open-circuit in the part. Therefore, the voltage across zener diode (\(V_z\)) is as (5).

\[
V_z = V_L = \frac{R_L}{R} \times \frac{V_I}{R_L} \tag{5}
\]

where

\[
R_L = \frac{R_1 V_z}{I} \tag{6}
\]

where

\[
I_L = \frac{V_z}{R_L} \tag{7}
\]

and

\[
I_R = \frac{V_R}{R} \tag{8}
\]

where

\[
V_R = V_I - V_z \tag{9}
\]

Therefore,

\[
I_R = \frac{V_R}{R_1} \tag{10}
\]

Hence, the maximum load resistance that will allow zener diode to put ON is (11).

\[
R_{1,\text{max}} = \frac{V_z R}{V_R} \tag{11}
\]

The resistance \(R_1\) is known by the design parameters as (12).

\[
R_1 = \frac{V_R}{I_R} \tag{12}
\]

3.5 Amplifying Stage

The amplifying unit of the design consists of limiting resistors, and NPN transistors work in conjunction to produce a fixed voltage as reference voltage to the input of comparator. By using KVL expression for collector current,

\[
V_{cc} - I_C R_C - V_{CE} - I_E R_E = 0 \tag{13}
\]

where \(I_E R_E\) is 0 and assume that \(I_C\) is approximately equal to \(I_E\). Therefore

\[
V_{cc} - I_C R_C - V_{CE} = 0 \tag{14}
\]

where

\[
I_C = \frac{V_{cc} - V_{CE}}{R_C} \tag{15}
\]

or

\[
I_C = \beta I_B \tag{16}
\]

hence

\[
I_B = \frac{I_C}{\beta} \tag{17}
\]

For the base current equation,

\[
V_{bb} - I_B R_B - V_{BE} - I_E R_E = 0 \tag{18}
\]

where \(I_E R_E\) is 0, therefore,

\[
V_{bb} - I_B R_B - V_{BE} = 0 \tag{19}
\]

Therefore, \(I_B\) of the amplifying unit is

\[
I_B = \frac{V_{bb} - V_{BE}}{R_B} \tag{20}
\]

where, \(V_{BE}\) is biasing voltage and it is 0.8 V, \(\beta\) is the gain \((hfe)\) and

\[
V_B = \frac{I_B}{R_B} \tag{21}
\]
3.6 Sensing and Comparator Stage

The thermistor and variable resistor form the potential diode to give a compared voltage (reference voltage) into the comparator.

\[ V_I = \frac{THM}{THM + V_R} \]  
(22)

and

\[ V(y) = V_I \]  
(23)

where THM is the Thermistor, \( V_R \) is the variable resistor, \( V(y) \) is the comparable voltage and \( V_I \) is the reference voltage. The maximum non-reference is 8.4 V and the voltage to be compared is also 8.4 V. This stipulated voltage is subject to changes, depending on the variable state of thermistor with the variable resistor. Therefore, the output of comparator gives

\[ V_O = k(V^+ - V^-) \]  
(24)

or

\[ V_O = \left(1 + \frac{R_1}{R_2}\right)V_z \]  
(25)

3.7 Switching Stage

The triggering operation of this design is done by transistor automatically (PNP type). The operations solely depend on the voltage fed into the base of the transistor from the output of comparator\([8]\).

From KVL method,

\[ R_B = \frac{V_O + V_{BE}}{I_B} \]  
(26)

By replacing (17) with \( I_B \) in (26),

\[ R_B = \frac{(V_O + V_{BE})\beta}{I_C} \]  
(27)

Furthermore, the relay operational system which fed from the based voltage through the PNP transistor (\( Q_3 \)) notes by

\[ I_{C_{sat}} = \beta I_{B_{sat}} \]  
(28)

Therefore, relay resistance is

\[ R_{relay} = \frac{V_{cc}}{I_{C_{sat}}} \]  
(29)

3.8 Heating Stage (Load)

The last unit in this design is the heater (Load). A 120 W tungsten lamp is used as the heat source to the chamber which determines the required temperature into the chamber.

The heat contents in the chamber could be determined by applying first law of thermodynamics as (30).

\[ Q = MC\theta \]  
(30)

where \( Q \) is quantity of heat, \( M \) is mass of air in the chamber, \( C \) is specific heat capacity of air and \( \theta \) is temperature changes.

To determine the mass of air in the incubator, density of air in the incubator (31) should be applied\([9]\).

\[ \theta = \frac{m}{v} \]  
(31)

Density of air is 1.2 \( \frac{kg}{m^3} \) at room temperature of 35°C and normal atmospheric pressure of 750 mmHg. Specific heat capacity of air is 0.24 \( \frac{kJ}{kg \cdot K} \). mass of air in the incubator is 113.4 kg and difference between temperature of the incubator and the normal room temperature is as follows

\[ (39 + 273)^\circ K - (32 + 273)^\circ K \Rightarrow 7^\circ K \]  
(34)

where 39°C is the temperature of the incubator, 35°C is the room temperature and 273 is the absolute zero temperature. Therefore, substitute all the above data in (30), yields 191 J. Hence, 1 J equivalent to 1 \( \frac{W}{s} \). then 120 W bulb will distribute 191 J of heat. Therefore, a bulb of 120 W can raise the temperature in the incubator from the normal room temperature to 39°C. For the purpose, 120 W bulb was used to discharge 190.5 J of heat content to the chamber in a second.

To determine the power (W) of the bulb required in the chamber is known by given project specification, i.e. AC supply is 240 V at 50 Hz and the current rating is 500 mA. Hence, power in watts is the product of given voltage (AC supply), the given current and power factor via (35).

\[ P = VI\cos\phi \]  
(35)

where \( \phi \) is 0. Hence, \( \cos\phi \) is one. Therefore, bulb of 120 W is suitable to withstand the design specification with respect to the desired heat content to the chamber.

4 Results and Discussion

In order to be convinced and also to actualize the target, testing must be carried out, which will definitely give correspondent output. The potentiometer value was set, and corresponding temperatures were recorded as shown in Table 1.

Table 1, shows that the thermistor resistance is indirectly proportional to the temperature in the incubator.
Table 1. Thermistor resistance against temperature

<table>
<thead>
<tr>
<th>Potentiometer value (kΩ)</th>
<th>4.7</th>
<th>5.0</th>
<th>4.0</th>
<th>2.6</th>
<th>1.6</th>
<th>1.0</th>
</tr>
</thead>
<tbody>
<tr>
<td>Thermistor Temp. (°C) RT†</td>
<td>30</td>
<td>33</td>
<td>35</td>
<td>40</td>
<td>45</td>
<td></td>
</tr>
<tr>
<td>Thermistor voltage (V)</td>
<td>4.7</td>
<td>4.7</td>
<td>3.7</td>
<td>2.6</td>
<td>1.6</td>
<td>1.0</td>
</tr>
</tbody>
</table>

† RT is the room ambient temperature.

Chamber. Based on data points in Table 1, the behavior of temperature versus potentiometer values is shown in Fig. 4.

The second test was conducted by testing the temperature against time as shown in Table 2.

From Table 2, it can deduce that temperature increases and decreases due to an instantaneous response of the thermistor with respect to time variant. And this proves that sensitivity of thermistor as compared to other types of temperature senses’ devices. Moreover, Fig. 5 shows the behavior of both temperature (left axis) and potentiometer values (right axis) as a function of time.

5 Conclusion

This design has been implemented and found to be working effectively with limited or zero hindrance, even in case of power failure, another source of heat has been a device i.e. the use of a lantern which feeds the heat to the chamber through the chimney constructed to filter the deposited carbon monoxide which harmful to the respiration of the embryo during development. And also the implementation can be maintained at a lower cost and there is going to be a high profit margin when the design is commercialized.

REFERENCES